Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1-4. (Canceled)

5. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming a first amorphous semiconductor film comprising silicon and germanium on an insulating surface wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film comprising silicon on ~~and in contact with~~ the first amorphous semiconductor film; [[and]]

crystallizing each of the first and second amorphous semiconductor films by irradiating with an excimer laser light; and

forming a gate insulating film on the crystallized second amorphous semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

6. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming at least an electrode on an insulating surface;

forming ~~an insulating film~~ a gate insulating film covering the electrode;

forming a first amorphous semiconductor film comprising silicon and germanium on the gate insulating film wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film comprising silicon on ~~and in contact with~~ the first amorphous semiconductor film; [[and]]

crystallizing each of the first and second amorphous semiconductor films by irradiating with an excimer laser light; and

forming an interlayer insulating film on the crystallized second amorphous semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

7-14. (Canceled)

15. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming a first amorphous semiconductor film ~~including~~ comprising silicon and germanium on an insulating surface wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film ~~including~~ comprising silicon on the first amorphous semiconductor film;

providing [[an]]a metal element ~~capable of promoting crystallization of silicon~~ in contact with the first amorphous semiconductor film or the second amorphous semiconductor film ~~after forming the second amorphous semiconductor film~~;

crystallizing each of the first and second amorphous semiconductor films by heating to form a first crystalline semiconductor film and a second crystalline semiconductor film, respectively; and

forming a gate insulating film on the second crystalline semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

16. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming a first amorphous semiconductor film ~~including~~ comprising silicon and an element having a larger atomic radius than silicon on an insulating surface wherein a concentration of said element is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film ~~including~~ comprising silicon on the first amorphous semiconductor film;

providing [[an]]a metal element ~~capable of promoting crystallization of silicon~~ in contact with the first amorphous semiconductor film or the second amorphous semiconductor film ~~after forming the second amorphous semiconductor film~~;

crystallizing each of the first and second amorphous semiconductor films by heating to form a first crystalline semiconductor film and a second crystalline semiconductor film, respectively; and

forming a gate insulating film on the second crystalline semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

17. (Previously Presented) A method according to claim 15, further comprising the step of:

irradiating with a laser light to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

18. (Previously Presented) A method according to claim 15, further comprising the step of:

irradiating with a light from one selected from the group consisting of a halogen lamp, a xenon lamp, a mercury lamp, a metal halide lamp as a light source to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

19. (Currently Amended) A method according to claim 15,

wherein each of the first and second semiconductor films is formed by a plasma CVD ~~apparatus,~~

~~wherein a turbo molecular pump is used in an exhaust means connected to a reaction chamber of the plasma CVD apparatus~~ method.

20-28. (Canceled)

29. (Previously Presented) A method according to claim 16, further comprising the step of:

irradiating with a laser light to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

30. (Previously Presented) A method according to claim 16, further comprising the step of:

irradiating with a light from one selected from the group consisting of a halogen lamp, a xenon lamp, a mercury lamp, a metal halide lamp as a light source to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

31. (Currently Amended) A method according to claim 16,

wherein each of the first and second semiconductor films is formed by a plasma CVD ~~apparatus,~~

~~wherein a turbo molecular pump is used in an exhaust means connected to a reaction chamber of the plasma CVD apparatus~~ method.

32-34. (Canceled)

35. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming a first amorphous semiconductor film comprising silicon and germanium on an insulating surface wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film comprising silicon on ~~and in contact with~~ the first amorphous semiconductor film;

providing [[an]]a metal element ~~capable of promoting crystallization of silicon~~ in contact with the first amorphous semiconductor film or the second amorphous semiconductor film; [[and]]

crystallizing each of the first and second amorphous semiconductor films by irradiating with a laser light; and

forming a gate insulating film on the crystallized second amorphous semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

36. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming at least an electrode on an insulating surface;

forming ~~an insulating film~~ a gate insulating film covering the electrode;

forming a first amorphous semiconductor film comprising silicon and germanium on the gate insulating film wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film comprising silicon on ~~and in contact with~~ the first amorphous semiconductor film;

providing [[an]]a metal element ~~capable of promoting crystallization of silicon~~ in contact with the first amorphous semiconductor film or the second amorphous semiconductor film; [[and]]

crystallizing each of the first and second amorphous semiconductor films by irradiating with a laser light; and

forming an interlayer insulating film on the crystallized second amorphous semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

37-38. (Cancelled)

39. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming a first amorphous semiconductor film comprising silicon and germanium on an insulating surface wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film comprising silicon on ~~and in contact with~~ the first amorphous semiconductor film;

providing [[an]]a metal element ~~capable of promoting crystallization of silicon~~ in contact with the first amorphous semiconductor film or the second amorphous semiconductor film; [[and]]

crystallizing each of the first and second amorphous semiconductor films by irradiating with an excimer laser light; and

forming a gate insulating film on the crystallized second amorphous semiconductor film after crystallizing each of the first and second amorphous semiconductor films.

40. (Currently Amended) A method of manufacturing a semiconductor device~~, said method~~ comprising the steps of:

forming at least an electrode on an insulating surface;

forming ~~an insulating film~~ a gate insulating film covering the electrode;

forming a first amorphous semiconductor film comprising silicon and germanium on the gate insulating film wherein a concentration of the germanium is within a range of 0.1 atom% to 10 atom%;

forming a second amorphous semiconductor film <u>comprising silicon</u> on ~~and in contact with~~ the first amorphous semiconductor film;

providing [[an]]<u>a metal</u> element ~~capable of promoting crystallization of silicon~~ in contact with the first amorphous semiconductor film or the second amorphous semiconductor film; [[and]]

crystallizing each of the first and second amorphous semiconductor films by irradiating with a laser light<u>; and</u>

<u>forming an interlayer insulating film on the crystallized second amorphous semiconductor film after crystallizing each of the first and second amorphous semiconductor films</u>.

41. (Previously Presented) A method according to claim 39, further comprising the step of:

irradiating with a laser light to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

42. (Previously Presented) A method according to claim 39, further comprising the step of:

irradiating with a light from one selected from the group consisting of a halogen lamp, a xenon lamp, a mercury lamp, a metal halide lamp as a light source to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

43. (Currently Amended) A method according to claim 39,

wherein each of the first and second semiconductor films is formed by a plasma CVD ~~apparatus, and~~

~~wherein a turbo molecular pump is used in an exhaust means connected to a reaction chamber of the plasma CVD apparatus~~ <u>method</u>.

44. (Previously Presented) A method according to claim 40, further comprising the step of:

irradiating with a laser light to obtain a higher crystallinity each of the first and second crystalline semiconductor films after the crystallizing step.

45. (Previously Presented) A method according to claim 40, further comprising the step of:

irradiating with a light from one selected from the group consisting of a halogen lamp, a xenon lamp, a mercury lamp, a metal halide lamp as a light source to obtain a higher crystallinity of each of the first and second crystalline semiconductor films after the crystallizing step.

46. (Currently Amended) A method according to claim 40,

wherein each of the first and second semiconductor films is formed by a plasma CVD ~~apparatus, and~~

~~wherein a turbo molecular pump is used in an exhaust means connected to a reaction chamber of the plasma CVD apparatus~~ method.

47. (Previously Presented) A method according to claim 5 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

48. (Previously Presented) A method according to claim 6 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

49. (Previously Presented) A method according to claim 15 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

50. (Previously Presented) A method according to claim 16 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

51. (Previously Presented) A method according to claim 35 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

52. (Previously Presented) A method according to claim 36 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

53. (Previously Presented) A method according to claim 39 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

54. (Previously Presented) A method according to claim 40 further comprising the step of patterning the first amorphous semiconductor film and the second amorphous semiconductor

film before crystallizing each of the first amorphous semiconductor film and the second amorphous semiconductor film.

55. (Previously Presented) A method according to claim 5,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

56. (Previously Presented) A method according to claim 6,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

57. (Previously Presented) A method according to claim 15,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

58. (Previously Presented) A method according to claim 16,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

59. (Previously Presented) A method according to claim 35,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

60. (Previously Presented) A method according to claim 36,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

61. (Previously Presented) A method according to claim 39,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

62. (Previously Presented) A method according to claim 40,

wherein a concentration of oxygen, nitrogen and carbon in the first and second amorphous semiconductor films are less than $1x10^{19}/cm^3$.

63. (Currently Amended) A method according to claim 15,

wherein the <u>metal</u> element is at least one selected from the group consisting of Fe, Ni, Co, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

64. (Currently Amended) A method according to claim 16,

wherein the <u>metal</u> element is at least one selected from the group consisting of Fe, Ni, Co, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

65. (Currently Amended) A method according to claim 35,

wherein the <u>metal</u> element is at least one selected from the group consisting of Fe, Ni, Co, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

66. (Currently Amended) A method according to claim 36,

wherein the <u>metal</u> element is at least one selected from the group consisting of Fe, Ni, Co, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

67. (Currently Amended) A method according to claim 39,

wherein the <u>metal</u> element is at least one selected from the group consisting of Fe, Ni, Co, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

68. (Currently Amended) A method according to claim 40,

wherein the metal element is at least one selected from the group consisting of Fe, Ni, Co, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

69. (Previously Presented) A method according to claim 5,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

70. (Previously Presented) A method according to claim 6,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

71. (Previously Presented) A method according to claim 15,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

72. (Previously Presented) A method according to claim 16,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

73. (Previously Presented) A method according to claim 35,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

74. (Previously Presented) A method according to claim 36,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

75. (Previously Presented) A method according to claim 39,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

76. (Previously Presented) A method according to claim 40,

wherein the first amorphous semiconductor film is thinner than the second semiconductor film.

77-92. (Canceled)

93. (New) A method according to claim 5, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

94. (New) A method according to claim 6, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

95. (New) A method according to claim 15, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

96. (New) A method according to claim 16, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

97. (New) A method according to claim 35, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

98. (New) A method according to claim 36, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

99. (New) A method according to claim 39, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.

100. (New) A method according to claim 40, wherein the second amorphous semiconductor film is formed so that a combined thickness of the first and second amorphous semiconductor films is within a range of 20-100 nm.